

## **Appendix A**

### **Incorporation by Reference of Commonly Owned Applications**

The following patent applications, commonly owned and filed on the same day as the present application, are hereby incorporated herein in their entirety by reference thereto:

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<b>Title</b>	<b>Application No.</b>	<b>Attorney Docket No.</b>
"Integration and Hold Phase Detection"		CCOM.003A
"Current Mode Phase Detection"		CCOM.004A
"Trigger Circuit"		CCOM.005A
"Two-Stage Multiplier Circuit"		CCOM.006A
"Reset Circuit"		CCOM.007A
"Multiplier Circuit"		CCOM.008A
"Frame Pattern Detection in an Optical Receiver"		CCOM.016A
"Single to Differential Input Buffer Circuit"		CCOM.017A
"Acquisition Aid Circuit"		CCOM.018A
"Low Voltage Differential Signaling Output Buffer"		CCOM.019A
"Low Frequency Loop-Back in a High-Speed Optical Transceiver"		CCOM.020A
"Phase Frequency Detector"		CCOM.021A
"Phase Alignment of Data to Clock"		CCOM.022A
"Voltage Controlled Oscillator"		CCOM.023A
"System and Method of Digital Tuning a Voltage Controlled Oscillator"		CCOM.024A
"System and Method of Tuning a Voltage Controlled Oscillator"		CCOM.025A
"High-Speed Output Driver"		CCOM.026A

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